



## Central Purchase unit

# National Institute of Technology Srinagar (J&K) -190 006

Email: [cpu@nitsri.ac.in](mailto:cpu@nitsri.ac.in)

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### Document for Tender No. 68 Dated 03.04.2019 "

Tender Notice for Invitation of bids for supply of Lab items for Machine Learning lab of Electronics & Comm. Engg. Department"

-0-0-0-

1. Sealed Bids are invited for supply of various equipment having detailed Specifications as given in Annexure –I, in a 2- bid format on the terms & conditions and as per the instructions given below from para-2, from interested eligible bidders:
- 2. Techno-Commercial Bid, Containing the following documents in Envelope- A:-**
  - (a) Proof of being Original equipment manufacturer or Distributor/Marketing agent/ Authorized dealer in the shape of copy of agreement and registration under law in force.
  - (b) The OEM (Original Equipment Manufacturer) of offered product should be ISO 9001: 2015 and ISO 14001:2015 certified or certified by any other national or international regulatory body. The products quoted must also be certified.
  - (c) PAN card & Tax registration document of the firm.
  - (d) ITR's of previous 3-years.
  - (e) Proof of annual turnover of the firm for last three years in such trade, which shall not be less than Rs.20.00 lacs., with at-least one such contract worth not less than Rs. 3.00 lacs or two such contracts worth Rs.5.00 Lacs to reputed Institutes/ organizations/ Departments during last three years.
  - (f) Proof of availability of after-sale support & service.
  - (g) Bid security for Rs.15,000/= in the shape of a BD/DD/TDR/FDR pledged in favour of Officer In-charge Central Purchase unit NIT Srinagar.
  - (h) Tender document fee Rs.500/- in the form of DD, favoring of Director, NIT Srinagar. Tender without bid security /document fee shall be rejected.
  - (i) Bid form in the format given in Annexure-II duly filled.
  - (j) List of organizations/ Institutions where such equipment has been supplied & installed successfully during last 3-years.
- 3. Price Bid Containing the following in Envelope-B:-**
  - (a) Prices offered and quoted both in words & figures. Prices must be offered /quoted in the format enclosed as Annexure-III in INR only. The rates quoted should be covered with transparent tape.
  - (b) The rate quoted should be FOR NIT Srinagar (J&K).
  - (c) Bid price should be without over writing. However minor over writing should be clearly signed by the bidder. In case of any discrepancy between price quoted in figures and words, the price quoted in words will prevail. Bid price should be firm for the bid validity period.
  - (d) Bid security for an amount equal to 3% of the offered cost in the form of CDR/ BD/ DD/TDR/FDR pledged to Officer In-charge, Central Purchase Unit NIT Srinagar (J&K)
  - (e) All duties, taxes and levies (GST or other charges) payable, must be quoted separately, but included in the total cost.
  - (f) As per SRO 129 of Government of Jammu & Kashmir, the institute is Exempted from State entry tax. The Institute will provide Entry tax exemption certificate to successful bidder with supply order.
- 4. Validity of Bids;**

Bids shall remain valid at least for 120 days from the date of opening of the bids.
- 5. Evaluation of Bids.**
  - (a) The purchaser shall first evaluate and compare the technical bids as per following criteria to determine the techno-commercially qualified bidders:
    - (i) Properly filled and signed as per given formats
    - (ii) Conform to terms and conditions and technical specifications.
    - (iii) Are accompanied with Bid security and all other documents.

**6. Award of contract.**

- (a) Contract shall be awarded to the bidder whose bid is commercially-technically qualified and is offered at lowest reasonable evaluated price.
- (b) Successful bidder shall be notified about the award of the contract through a supply order where- in terms and conditions of supply shall be incorporated.

**7. Payment & Performance Guarantee:**

100% (90% + 10%) payment shall be made after successful receipt of goods in good condition and accepted as per prescribed specifications by the nominated committee and after successful installation & commissioning. 10% will however be retained as security deposit against performance guarantee for the warranty period.

**8. Warranty and after sale service:**

- (a) All items shall carry comprehensive standard warranty from the date of installation & commissioning and service support after expiry of warranty.

**9. Settlement of disputes.**

Settlement of disputes if any shall be subject to the jurisdiction of Srinagar Courts only.

**10. Liquidated Damages.**

If the bidder after accepting the purchase order of goods/equipments or services, fails to deliver any or all of the goods/equipments or to perform the services with -in the specified period, a penalty of 0.50%(half percent) of the price value of the item per week or part-thereof shall be imposed. NIT Srinagar shall proceed on its own to consider the termination of the supply order, on the risk & responsibility of the defaulting bidder, in case it is found that supplier is not interested to supply and delay is causing academic loss.

**11. Submission of Bids.**

- (a) The last date for submission of bids is **24.04..2019 up-to 4.00PM.**
- (b) Bids should be properly sealed.
- (c) The two envelopes A & B should be separately sealed and superscripted as Techno-Commercial Bid & Price Bid. These two sealed envelopes should be sealed in a bigger Envelop all 03 envelopes shall be specific with the address of the Tender receiving Officer; Tender Number; due date of submission, Name of equipment/item/department and name of bidder written neatly. (d) Bids should be addressed to Officer In-charge, Central Purchase Unit NIT Srinagar (J&K)
- (e) Bids not from Srinagar shall be dispatched sufficiently well in advance so as to reach the destination as per scheduled time & date. NIT Srinagar shall not be responsible for any delay caused by Post or Courier agency in neat and bold letters.

**12. Bid opening.**

- (a) The Technical Bid (Envelope- A) will be opened first on **25.04.2019 at 2.30 pm** in the office room of the undersigned in the administrative block of the Institute. Price Bid (Envelope-B) of the qualified bidders as detailed above will be opened subsequently. Interested bidders can attend the Bid opening.

**13.** Notwithstanding above, the purchaser reserves the right to reject any or all the bids received in response to this N.I.T. or withdraw it without assigning any reasons thereof.

**14.** For any future information or corrigendum or addendum regarding this tender, please be in touch with our website: [www.nitsri.ac.in](http://www.nitsri.ac.in).

**15.** Any clarification may sought on the following emails:

- Lab In-Charge: [ahmir@nitsri.net](mailto:ahmir@nitsri.net)
- O/C, Central purchase Unit: [cpu@nitsri.ac.in](mailto:cpu@nitsri.ac.in)

Officer In-charge  
Central Purchase unit, NIT Srinagar  
Email: [cpu@nitsri.ac.in](mailto:cpu@nitsri.ac.in)

N.B.

- 1. Before preparing your valuable bid kindly go through the document fully and take care of all the requirements.
- 2. Bidders from outside Srinagar may please send their Bids well in advance so that these are

received in time.

No. NIT/CPU/19/4813

Dated 03.04.2019

**Annexure-I**

**“Name & amp;/Specifications of equipments ”**

**DE2-115 Development and Education Board:**

S.No.	Description of the item	Specification/	Quantity	
1	Board	DE2-115 Board	03 No's	
	FPGA	Cyclone® IV EP4CE115		
		114,480 logic elements (LEs		
		3,888 Embedded memory (Kbits)		
		266 Embedded 18 x 18 multipliers		
		4 General-purpose PLLs		
		528 User I/Os		
	Configuration Device and USB-Blaster Circuit	EPCS64 serial configuration device		
		On-board USB-Blaster circuitry		
		JTAG and AS mode configuration supported		
	Memory Devices	128MB (32Mx32bit) SDRAM		
		2MB (1Mx16) SRAM		
		8MB (4Mx16) Flash with 8-bit mode		
		32Kbit EEPROM		

**DE5-Net FPGA Development Kit.**

S.No	Description of the item	Specification	Quantity	
2	Board	DE5-Net Board	01 No	
	FPGA	Altera Stratix® V GX FPGA (5SGXEA7N2F45C2)		
	FPGA Configuration	On-Board USB Blaster II or JTAG header for FPGA programming		
		Fast passive parallel (FPPx32) configuration via MAX II CPLD and flash memory		
	memory	Memory Two Independent DDR3 SODIMM Socket, Up to 8GB 800 MHz or 4GB 933 MHz for each socket		
		Four Independent 550MHz SRAM, 18-bits data bus and 72Mbit for each		
256MB FLASH				

**Annexure-II**  
**BID CHECK LIST**  
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To,  
Officer In-charge,  
Central Purchase unit,  
NIT Srinagar.

Ref: Tender No. \_\_\_\_\_ issued under No.: NITS/CPU/ 2019/.....  
Dated..... for-----  
For .....Department

Sir,  
With reference to above invitation for bids, we would like to say that we have gone through your bid document thoroughly and hence offer our competitive Technical/Price Bid in sealed envelope for the supply of various goods/equipment listed in your document.

The following documents constitute our Bid:

- Technical specification/ literature attached:- Yes/No
- Valid tax clearance certificate attached:- Yes/ No
- OEM/ Authorized Dealership / certificate attached Yes/ No
- Revenue stamp affixed. Yes/ No
- Rates covered with transparent tape:- Yes/ No
- Bid document fee deposited:- Yes/ No
- Bid Security Receipt enclosed:- Yes/ No
- Bid price in Indian Rupees and as per format given in annexure-III:- Yes/ No
- Rater are FOR Srinagar including Installation/commissioning/  
Training if required. Yes/No
- Bid without correction/overwriting:- Yes/ No
- Price Bid schedule in the requisite format
- List of organization where this equipment has been supplied/Installed

Yours Sincerely

From M/S.....

Place:

Telephone No.....Email:

Date.....

**Annexure-III**

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**Price Schedule**

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<b>S. No</b>	<b>Name of equipment/ Goods /item</b>	<b>Basic cost</b>	<b>GST</b>	<b>Any other tax</b>	<b>Total unit price</b>	<b>Quantity</b>	<b>Total Price</b>
1							
2							
3							
4							
5							

